

Important:

*for PCI 2.1,2.2 combo application, remove L3

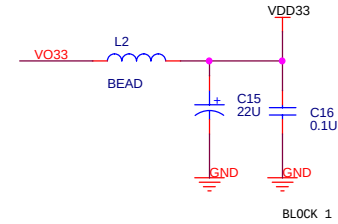
*for PCI 2.2 only application, remove L3,D2,JP2

*for non Wake On LAN application, remove D1,D2,D3,JP2,L4

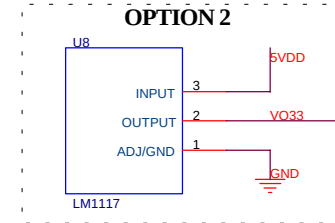
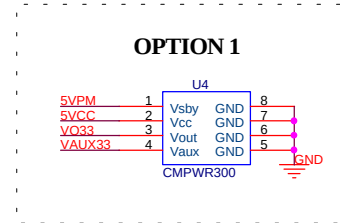
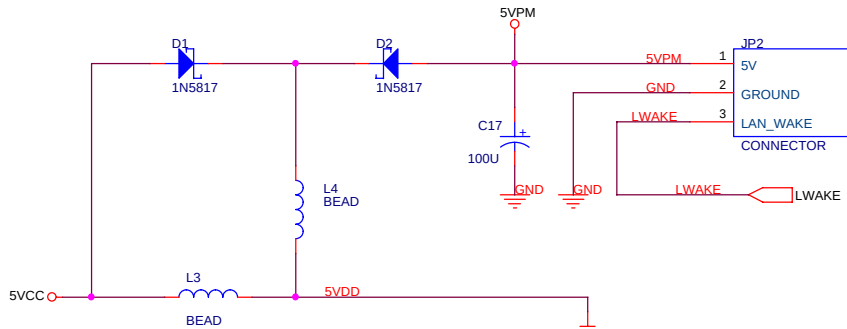
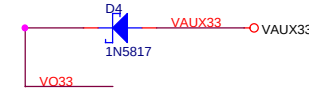
*FOR LUMP VAUX, C17 MUST BE LARGE ENOUGH TO AVOID POWER SUPPLY'S COMPATIBILITY

*for RTL8139C/RTL8139C+ application, all bead must be rated 300mA/100ohm@100Mhz

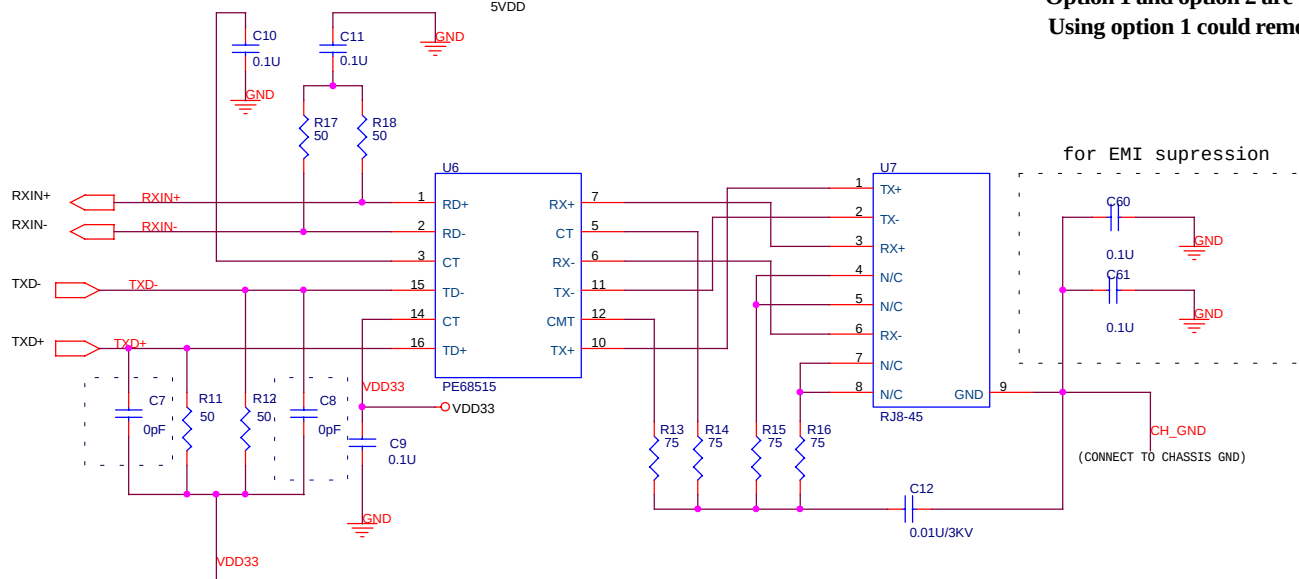
* C7 and C8 are reserved for future use



BLOCK 1



*Option 1 and option 2 are used for 5V -> 3.3V power.
Using option 1 could remove D1,D2,D3,L3 and L4.



Title		
Realtek RTL8139C/RTL8139C+ application circuit -- LAN Card		
Size	Document Number	Rev
B	RED139C_02_109	
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